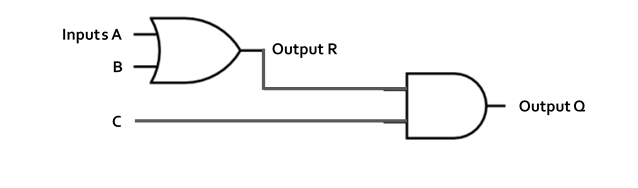
**Logic Retrieval Quiz**

**1**: Label each logic gate, the complete the truth tables for each logic gate.

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | |  |  | | |  |  | | |
| **NOT** | | **AND** | | |  | **OR** | | |
|  | |  | | |  |  | | |
| A | OUT | A | B | OUT |  | A | B | OUT |
| 1 | 0 | 1 | 1 | 1 |  | 1 | 1 | 1 |
| 0 | 1 | 1 | 0 | 0 |  | 1 | 0 | 1 |
|  | | 0 | 1 | 0 |  | 0 | 1 | 1 |
|  | | 0 | 0 | 0 |  | 0 | 0 | 0 |

**2**: Complete the truth table for the following circuit.



|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **A** | **B** | **C** | **R** | **Q** |
| 1 | 1 | 1 | 1 | 1 |
| 1 | 1 | 0 | 1 | 0 |
| 1 | 0 | 1 | 1 | 1 |
| 1 | 0 | 0 | 1 | 0 |
| 0 | 1 | 1 | 1 | 1 |
| 0 | 1 | 0 | 1 | 0 |
| 0 | 0 | 1 | 0 | 0 |
| 0 | 0 | 0 | 0 | 0 |

**3:** Create a ‘Logic Circuit Diagram’ for each of the given expressions.

|  |  |
| --- | --- |
| **Expression** | **Circuit** |
| P = NOT (A OR B) |  |
| P = (A AND B) OR C |  |
| P= NOT ((A AND B) AND C) |  |